Preferred Device

Complementary Plastic Silicon Power Transistors

The MJE170/180 series is designed for low power audio amplifier and low current, high speed switching applications.

Features

- Pb-Free Package is Available
- Collector-Emitter Sustaining Voltage -

V_{CEO(sus)} = 40 Vdc - MJE170, MJE180 = 60 Vdc - MJE171, MJE181 = 80 Vdc - MJE172, MJE182

• DC Current Gain -

 h_{FE} = 30 (Min) @ I_C = 0.5 Adc = 12 (Min) @ I_C = 1.5 Adc

• Current-Gain - Bandwidth Product -

 $f_T = 50 \text{ MHz (Min)} @ I_C = 100 \text{ mAdc}$

• Annular Construction for Low Leakages -

 I_{CBO} = 100 nA (Max) @ Rated V_{CB}

• Epoxy Meets UL 94 V-0 @ 0.125 in

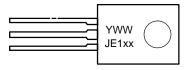
• ESD Ratings: Machine Model, C Human Body Model, 3B ON Semiconductor®

http://onsemi.com

3 AMPERES POWER TRANSISTORS COMPLEMENTARY SILICON 40 - 60 - 80 VOLTS 12.5 WATTS



MARKING DIAGRAM



JE1xx = Specific Device Code

Y = Year WW = Work Week

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

MAXIMUM RATINGS

Rating	Symbol	MJE170 MJE180	MJE171 MJE181	MJE172 MJE182	Unit
Collector-Base Voltage	V _{CB}	60	80	100	Vdc
Collector–Emitter Voltage	V _{CEO}	40	60	80	Vdc
Emitter-Base Voltage	V _{EB}		7.0	•	Vdc
Collector Current – Continuous Peak	Ic		3.0 6.0		Adc
Base Current	I _B		1.0		Adc
Total Power Dissipation @ T _A = 25°C Derate above 25°C	P _D		1.5 0.012		W/°C
Total Power Dissipation @ T _C = 25°C Derate above 25°C	P _D	12.5 0.1		W W/°C	
Operating and Storage Junction Temperature Range	T _J , T _{stg}		-65 to +150		°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction–to–Case	θЈС	10	°C/W
Thermal Resistance, Junction–to–Ambient	$\theta_{\sf JA}$	83.4	°C/W

ORDERING INFORMATION

Device	Package	Shipping [†]
MJE170	TO-225	500 Units / Box
MJE171	TO-225	500 Units / Box
MJE172	TO-225	500 Units / Box
MJE180	TO-225	500 Units / Box
MJE181	TO-225	500 Units / Box
MJE182	TO-225	500 Units / Box
MJE182G	TO-225 (Pb-Free)	500 Units / Box

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

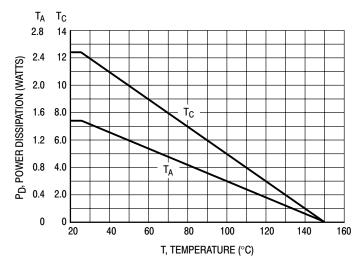


Figure 1. Power Derating

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Sustaining Voltage (I _C = 10 mAdc, I _B = 0)	MJE170, MJE180 MJE171, MJE181 MJE172, MJE182	V _{CEO(sus)}	40 60 80	_ _	Vdc
	MJE170, MJE180 MJE171, MJE181 MJE172, MJE182 MJE170, MJE180 MJE171, MJE181 MJE172, MJE182	Ісво	- - - -	0.1 0.1 0.1 0.1 0.1	μAdc mAdc
Emitter Cutoff Current (V _{BE} = 7.0 Vdc, I _C = 0)		I _{EBO}	-	0.1	μAdc
ON CHARACTERISTICS		•	•		
DC Current Gain ($I_C = 100 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}$) ($I_C = 500 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}$) ($I_C = 1.5 \text{ Adc}, V_{CE} = 1.0 \text{ Vdc}$)		h _{FE}	50 30 12	250 - -	-
Collector–Emitter Saturation Voltage ($I_C = 500 \text{ mAdc}$, $I_B = 50 \text{ mAdc}$) ($I_C = 1.5 \text{ Adc}$, $I_B = 150 \text{ mAdc}$) ($I_C = 3.0 \text{ Adc}$, $I_B = 600 \text{ mAdc}$)		V _{CE(sat)}	- - -	0.3 0.9 1.7	Vdc
Base–Emitter Saturation Voltage ($I_C = 1.5$ Adc, $I_B = 150$ mAdc) ($I_C = 3.0$ Adc, $I_B = 600$ mAdc)		V _{BE} (sat)	_ _	1.5 2.0	Vdc
Base–Emitter On Voltage (I _C = 500 mAdc, V _{CE} = 1.0 Vdc)		V _{BE(on)}	-	1.2	Vdc
DYNAMIC CHARACTERISTICS		•	•	•	
Current-Gain - Bandwidth Product (Note 1) (I _C = 100 mAdc, V _{CE} = 10 Vdc, f _{test} = 10 MHz)		f _T	50	_	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 0.1 MHz)	MJE171/MJE172 MJE181/MJE182	C _{ob}	_ _	60 40	pF

^{1.} $f_T = |h_{fe}| \cdot f_{test}$.

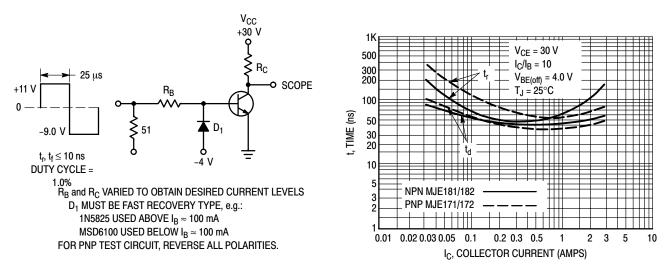


Figure 2. Switching Time Test Circuit

Figure 3. Turn-On Time

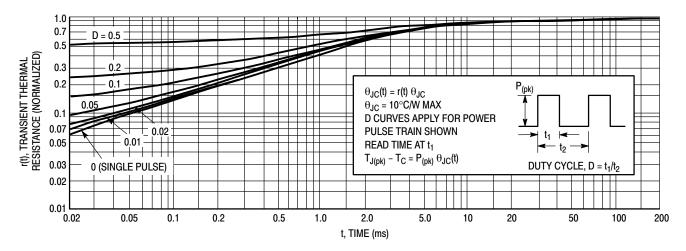


Figure 4. Thermal Response

ACTIVE-REGION SAFE OPERATING AREA

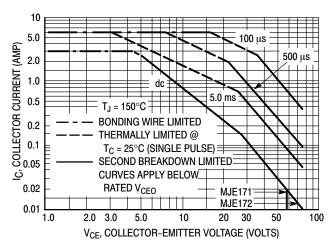


Figure 5. MJE171, MJE172

There are two limitations on the power handling ability of a transistor – average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figures 5 and 6 is based on $T_{J(pk)} = 150$ °C; T_{C} is variable depending on conditions. Second breakdown

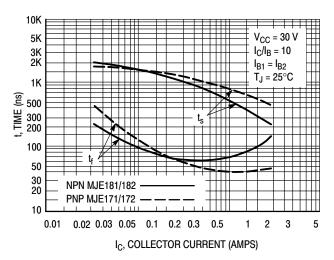


Figure 7. Turn-Off Time

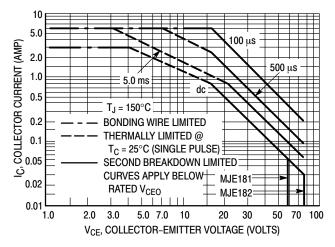


Figure 6. MJE181, MJE182

pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} < 150$ °C. $T_{J(pk)}$ may be calculated from the data in Figure 4. At high case temperature, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

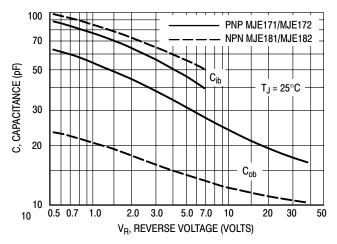
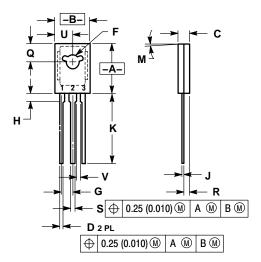


Figure 8. Capacitance

PACKAGE DIMENSIONS

TO-225 CASE 77-09 ISSUE Z



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
 077-01 THRU -08 OBSOLETE, NEW STANDARD

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.425	0.435	10.80	11.04	
В	0.295	0.305	7.50	7.74	
С	0.095	0.105	2.42	2.66	
D	0.020	0.026	0.51	0.66	
F	0.115	0.130	2.93	3.30	
G	0.094 BSC		2.39 BSC		
Н	0.050	0.095	1.27	2.41	
J	0.015	0.025	0.39	0.63	
K	0.575	0.655	14.61	16.63	
M	5°	5° TYP		TYP	
Q	0.148	0.158	3.76	4.01	
R	0.045	0.065	1.15	1.65	
S	0.025	0.035	0.64	0.88	
U	0.145	0.155	3.69	3.93	
٧	0.040		1.02		

STYLE 1:

EMITTER

COLLECTOR

BASE

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