Monolithic Digital IC



Overview

The LB1640N is a motor driver IC with a forward/reverse control feature. This IC is optimal for driving motors used in front-loading VCRs and auto-reverse cassette decks.

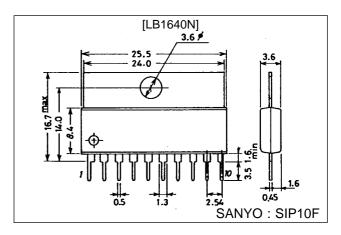
Features

- Brake function on chip
- Dash current absorption diode on chip
- Broad operating voltage range (4 to 18 V)
- Direct drive made possible by TTL

Package Dimensions

unit : mm

3046B-SIP10F



Specifications

Absolute Maximum Ratings at Ta = 25 °C

| Parameter | Symbol | Conditions | Ratings | Unit |
|-----------------------------|--------------------|--|-------------------------|------|
| Maximum supply voltage | V _{CC} | | 20 | V |
| Input voltage | V _{IN} | | -0.3 to V _{CC} | V |
| Output current | I _O max | t = 5 ms, with cycle time of 5 sec. or more | 1.6 | A |
| Allowable power dissipation | Pd max | No heat sink | 2.5 | W |
| | | When using heat sink (100 x 100 x 1.5 mm ³) | 7.0 | W |
| Operating temperature | Topr | | -25 to +75 | °C |
| Storage temperature | Tstg | | -55 to +125 | °C |

Allowable Operating Ranges at Ta = 25 $^{\circ}C$

| Parameter | Symbol | Ratings | Unit |
|--|------------------|----------------------|------|
| Supply voltage | V _{CC} | 4 to 18 | V |
| High-level input voltage | V _{IH} | 3 to V _{CC} | V |
| Low-level input voltage | V _{IL} | -0.3 to +0.4 | V |
| Output current | Ι _Ο | -500 to +500 | mA |
| Forward \leftrightarrow Reverse inhibit time | T _{OFF} | 10 or longer | μs |

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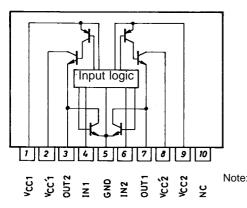
Electrical Characteristics at Ta = 25 °C, $V_{\rm CC}$ = $V_{\rm CC}$ ' = 12 V

| Parameter | Symbol | Output | | typ | max | Unit |
|---------------------------|-----------------------------------|--|------|-----|------|------|
| Supply Current | Icc | $V_11 \text{ or } V_12 = 3 \text{ V}$, $R_L = \infty$, $V_{CC} = V_{CC}' = 16 \text{ V}$ | | | 40 | mA |
| High-level output voltage | V _{OH} 1 | $V_11 \text{ or } V_12 = 3 \text{ V}$, $I_0 = -300 \text{ mA}$ | 10.8 | | | V |
| | V _{OH} 2 | $V_11 \text{ or } V_12 = 3 \text{ V}$, $I_0 = -500 \text{ mA}$ | 10.7 | | | V |
| Low-level output voltage | V _{OL} 1 | $V_11 \text{ or } V_12 = 3 \text{ V}$, $I_0 = 300 \text{ mA}$ | | | 0.5 | V |
| | V _{OL} 2 | $V_11 \text{ or } V_12 = 3 \text{ V}$, $I_0 = 500 \text{ mA}$ | | | 0.65 | V |
| Interoutput voltage | V ₀ 1-V ₀ 2 | $V_11 \text{ or } V_12 = 3 \text{ V}$, $I_0 = \pm 300 \text{ mA}$ | 10.3 | | | V |
| Input voltage | VI | I _I = 500 μA | 3 | | | V |
| Output leakage current | I _{O Leak} | $V_{CC} = V_{CC}' = 20 V$ $V_{IN}1 = V_{IN}2 = 0 V, V_{O} = 20 V \text{ or } 0 V$ | | | ±100 | μA |

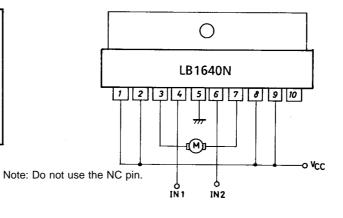
Control Modes

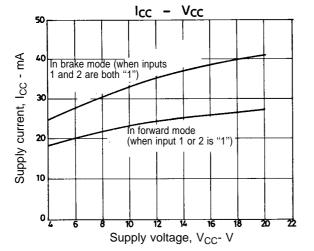
| Input | | Output | | - Remarks | |
|-------|---|--------|---|-----------|--|
| 1 | 2 | 1 | 2 | Remarks | |
| 0 | 0 | — | — | Open | |
| 1 | 0 | 1 | 0 | Forward | |
| 0 | 1 | 0 | 1 | Reverse | |
| 1 | 1 | 0 | 0 | Brake | |

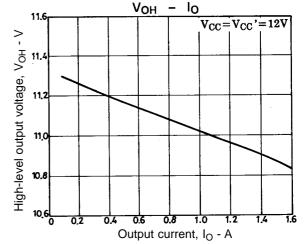
Equivalent Circuit Block Diagram

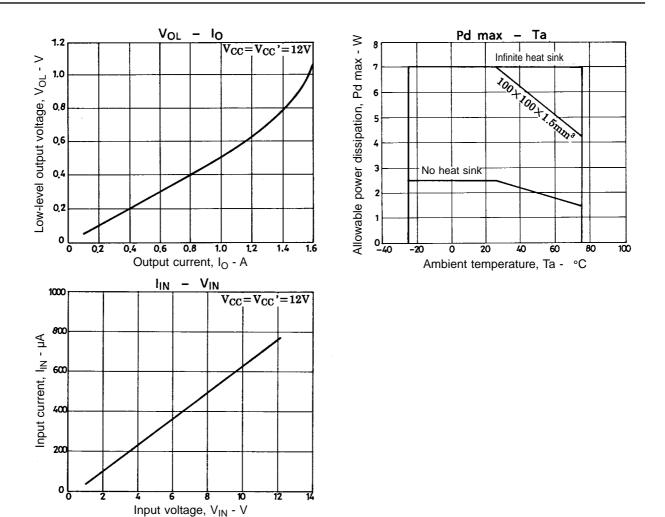


Sample Application Circuit









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