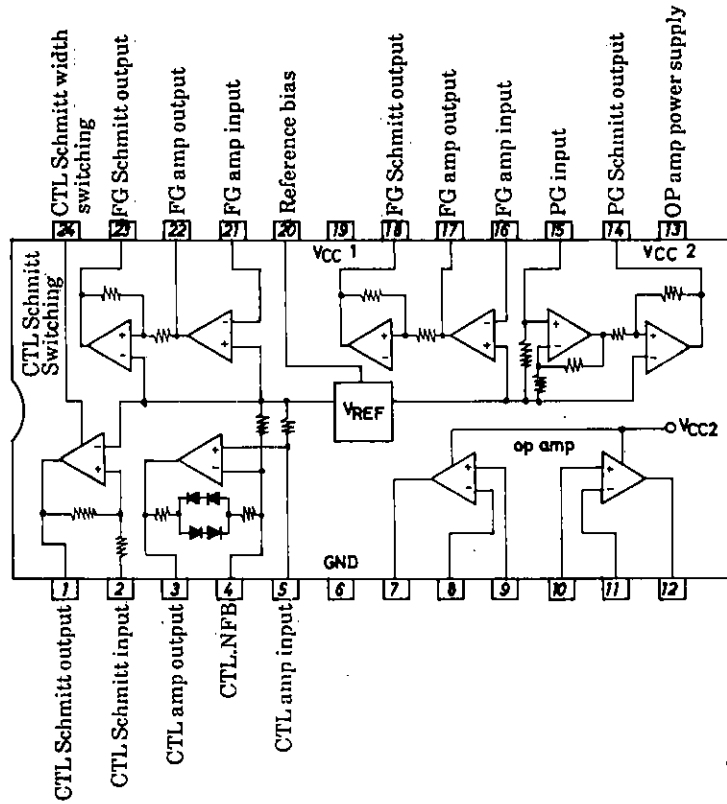




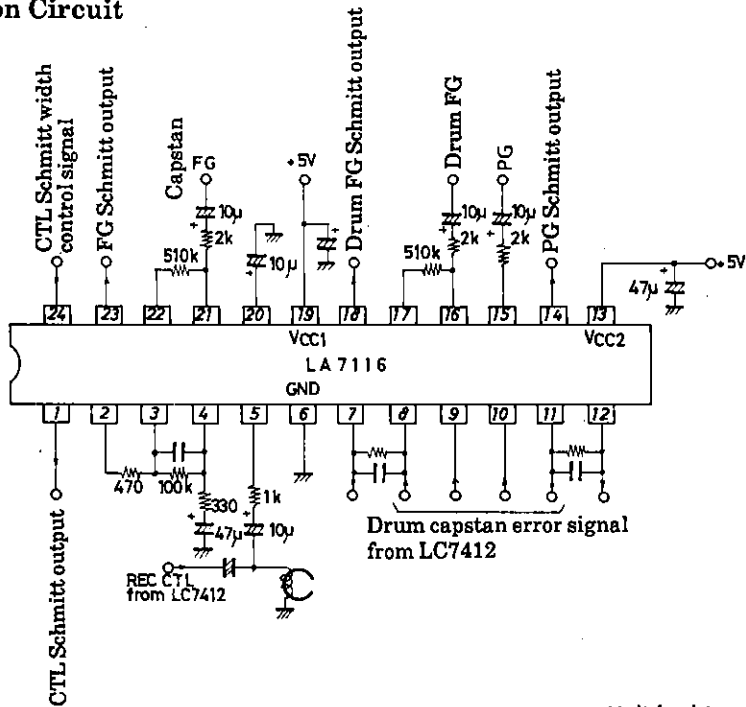
## LA7116

Continued from preceding page.			min	typ	max	unit
CTL Output Voltage	V <sub>OHCTL</sub>	I <sub>1</sub> = +0.5mA	4.0			V
	V <sub>OLCTL</sub>	I <sub>1</sub> = -0.5mA			1.0	V
PG Output Voltage	V <sub>OHPG</sub>	I <sub>14</sub> = +0.5mA	4.0			V
	V <sub>OLPG</sub>	I <sub>14</sub> = -0.5mA			1.0	V
FG Output Voltage	V <sub>OHFG1</sub>	I <sub>18</sub> = +0.5mA	4.0			V
	V <sub>OLFG1</sub>	I <sub>18</sub> = -0.5mA			1.0	V
	V <sub>OHFG2</sub>	I <sub>23</sub> = +0.5mA	4.0			V
	V <sub>OLFG2</sub>	I <sub>23</sub> = -0.5mA			1.0	V
CTL Amp Gain	G <sub>CTL</sub>	SG1:500Hz,1Vp-p,V <sub>3</sub> =1Vp-p	48	50	52	dB
CTL Amp Frequency Characteristic	ΔG <sub>CTL</sub>	SG1:10Hz,1Vp-p,V <sub>3</sub> =1Vp-p	-6	-2		dB
FG Amp Gain	G <sub>FG1</sub>	SG3:500Hz,1Vp-p,V <sub>17</sub> =1Vp-p	46	48	50	dB
	G <sub>FG2</sub>	SG4:500Hz,1Vp-p,V <sub>22</sub> =1Vp-p	46	48	50	dB
FG Amp Frequency Characteristic	ΔG <sub>FG1</sub>	SG3:20kHz,1Vp-p, V <sub>17</sub> =1Vp-p	-10	-6		dB
	ΔG <sub>FG2</sub>	SG4:20kHz,1Vp-p, V <sub>22</sub> =1Vp-p	-10	-6		dB
PG Schmitt Width	V <sub>HPG</sub>	SG2:500Hz	48	60	72	mVp-p
FG Schmitt Width	V <sub>HFG1</sub>	SG3:500Hz	185	230	275	mVp-p
	V <sub>HFG2</sub>	SG4:500Hz	185	230	275	mVp-p
CTL Schmitt Width	V <sub>HCTL1</sub>	SG1:500Hz,S1 = a	160	200	240	mVp-p
CTL Schmitt Width (Search)	V <sub>HCTL2</sub>	SG1:500Hz,S1 = b	320	400	480	mVp-p
CTL Schmitt Width (Slow)	V <sub>HCTL3</sub>	SG1:500Hz,S1 = c	+72	+92	+112	mV
CTL Schmitt Width (Slow)	V <sub>HCTL4</sub>	SG1:500Hz,S1 = c	+34	+54	+70	mV
CTL Schmitt Width (Switching Level)	V <sub>24H</sub>	S1 = d	3.0	3.5	4.0	V
	V <sub>24L</sub>	S1 = d	1.0	1.5	2.0	V
[OP Amp Characteristics] at V <sub>CC</sub> = 5 to 12V						
Circuit Current	I <sub>CC 2</sub>		0.3	0.8	1.2	mA
Input Offset Voltage	V <sub>IO 1</sub>			±2	±7	mV
	V <sub>IO 2</sub>			±2	±7	mV
Input Offset Current	I <sub>IO 1</sub>			±5	±50	nA
	I <sub>IO 2</sub>			±5	±50	nA
Input Bias Current	I <sub>B 1</sub>			45	250	nA
	I <sub>B 2</sub>			45	250	nA
Output Current (Source)	I <sub>OSOC 1</sub>		10			mA
	I <sub>OSOC 2</sub>		10			mA
Output Current (Sink)	I <sub>OSNK 1</sub>		10			mA
	I <sub>OSNK 2</sub>		10			mA
Common-Mode Input Voltage Range	V <sub>ICM</sub>		0	V <sub>CC</sub> to 1.5		V
Output Voltage Range	V <sub>OUT</sub>		0	V <sub>CC</sub> to 1.5		V

Equivalent Circuit Block Diagram



Sample Application Circuit



Unit (resistance :  $\Omega$  , capacitance : F)

- No products described or contained herein are intended for use in surgical implants, life-support systems, aerospace equipment, nuclear power control systems, vehicles, disaster/crime-prevention equipment and the like, the failure of which may directly or indirectly cause injury, death or property loss.
- Anyone purchasing any products described or contained herein for an above-mentioned use shall:
  - ① Accept full responsibility and indemnify and defend SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors and all their officers and employees, jointly and severally, against any and all claims and litigation and all damages, cost and expenses associated with such use:
  - ② Not impose any responsibility for any fault or negligence which may be cited in any such claim or litigation on SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors or any of their officers and employees jointly or severally.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of **December, 1996**. Specifications and information herein are subject to change without notice.