

# **MPSW3725**



### **NPN Transistor**

This device is designed for high current, low impedance line driver applications. Sourced from Process 26.

# Absolute Maximum Ratings TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
$V_{CEO}$	Collector-Emitter Voltage	40	V	
V <sub>CBO</sub>	Collector-Base Voltage	60	V	
V <sub>EBO</sub>	Emitter-Base Voltage	6.0	V	
I <sub>C</sub>	Collector Current - Continuous	1.2	А	
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C	

<sup>\*</sup>These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

1) These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

### Thermal Characteristics TA = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units
		MPSW3725	
P <sub>D</sub>	Total Device Dissipation	1.0	W
	Derate above 25°C	8.0	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	50	°C/W

(continued)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
OEE CUAI	RACTERISTICS					
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	40	i	ī	V
	Voltage*	, -				_
$V_{(BR)CES}$	Collector-Emitter Breakdown Voltage	$I_C = 10 \mu A, V_{BE} = 0$	60			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 100  \mu A,  I_{CE} = 0$	60			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 10  \mu A,  I_C = 0$	6.0			V
I <sub>CBO</sub>	Collector Cutoff Current	$V_{CB} = 50 \text{ V}, I_{E} = 0$			100	nA
		$V_{CB} = 50 \text{ V}, I_{E} = 0, T_{A} = 100^{\circ}\text{C}$	ļ		10	μА
ON CHAR	ACTERISTICS*  DC Current Gain	I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 1.0 V	30		100	
		$I_C = 100 \text{ mA}, V_{CE} = 1.0 \text{ V}$	60		180	
		$I_C=100$ mA, $V_{CE}=1.0$ V, $T_A=-55$ °C $I_C=300$ mA, $V_{CE}=1.0$ V	30 40			
		$I_C = 500 \text{ mA}, V_{CE} = 1.0 \text{ V}$ $I_C = 500 \text{ mA}, V_{CE} = 1.0 \text{ V}$	35			
		I <sub>C</sub> =500mA,V <sub>CE</sub> =1.0V,T <sub>A</sub> =-55°C	20 20			
		$I_C = 800 \text{ mA}, V_{CE} = 2.0 \text{ V}$ $I_C = 1.0 \text{ A}, V_{CE} = 5.0 \text{ V}$	25			
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	$I_C = 10 \text{ mA}, I_B = 1.0 \text{ mA}$			0.25	V
		$I_C = 100 \text{ mA}, I_B = 10 \text{ mA}$ $I_C = 300 \text{ mA}, I_B = 30 \text{ mA}$			0.26 0.4	l v
		$I_C = 500 \text{ mA}, I_B = 50 \text{ mA}$			0.52	V
		$I_C = 800 \text{ mA}, I_B = 80 \text{ mA}$			0.8 0.95	V V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	$I_C = 1.0 \text{ A}, I_B = 100 \text{ mA}$ $I_C = 10 \text{ mA}, I_B = 1.0 \text{ mA}$			0.93	V
· DL(Sat)		$I_C = 100 \text{ mA}, I_B = 10 \text{ mA}$			0.86	V
		$I_C = 300 \text{ mA}, I_B = 30 \text{ mA}$ $I_C = 500 \text{ mA}, I_B = 50 \text{ mA}$			1.1 1.2	V V
		$I_C = 800 \text{ mA}, I_B = 80 \text{ mA}$			1.5	V
		$I_C = 1.0 \text{ A}, I_B = 100 \text{ mA}$			1.7	V
SMALL SI	GNAL CHARACTERISTICS					
f <sub>T</sub>	Current Gain - Bandwidth Product	$I_C = 50 \text{ mA}, V_{CE} = 10 \text{ V},$ f = 100  MHz	250			MHz
C <sub>obo</sub>	Output Capacitance	$V_{CB} = 10 \text{ V}, I_{E} = 0,$ f = 1.0 MHz			25	pF
C <sub>ibo</sub>	Input Capacitance	$V_{EB} = 0.5 \text{ V}, I_{C} = 0,$ f = 1.0 MHz			100	pF
SWITCHII	NG CHARACTERISTICS					
ton	Turn-on Time	V <sub>CC</sub> = 30 V, V <sub>BE</sub> = 3.8 V,		22		ns

 $I_C = 500 \text{ mA}, I_{B1} = 50 \text{ mA}$ 

 $V_{CC} = 30 \text{ V}, I_{C} = 500 \text{mA}$ 

 $I_{B1} = I_{B2} = 50 \text{ mA}$ 

10

12

250

235

15

ns

ns

ns

ns

ns

Delay Time

Rise Time

Fall Time

Turn-off Time

Storage Time

 $t_{\text{d}}$ 

tr

t<sub>off</sub>

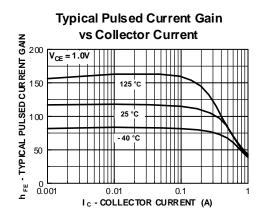
 $t_{\text{\tiny S}}$ 

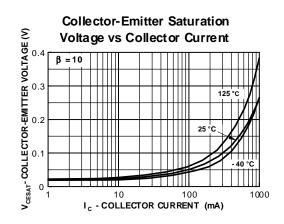
tf

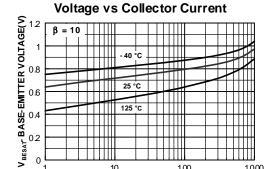
<sup>\*</sup>Pulse Test: Pulse Width  $\leq\!300~\mu\text{s},$  Duty Cycle  $\leq\!1.0\%$ 

(continued)

# **Typical Characteristics**







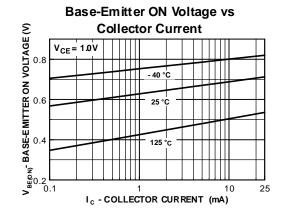
Ic - COLLECTOR CURRENT (mA)

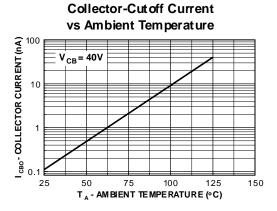
100

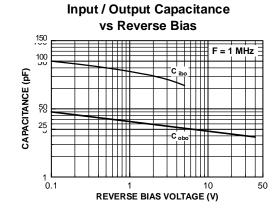
1000

0 L

**Base-Emitter Saturation** 

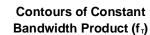


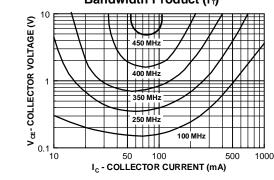




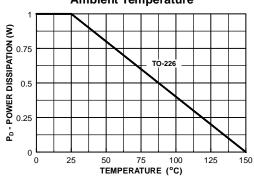
(continued)

# Typical Characteristics (continued)



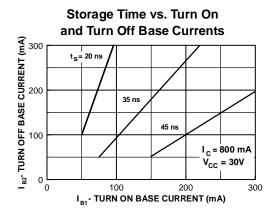


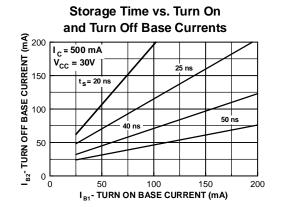
#### Power Dissipation vs Ambient Temperature

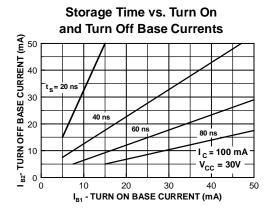


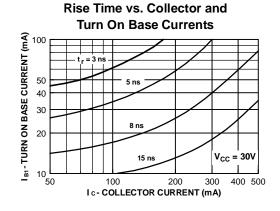
(continued)

# Typical Characteristics (continued)



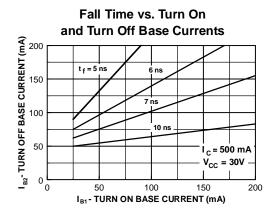


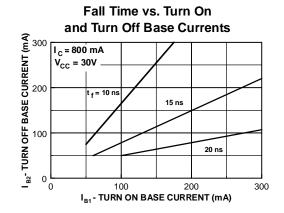


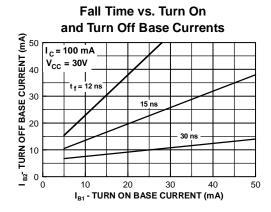


(continued)

### Typical Characteristics (continued)







(continued)

### **Test Circuit**

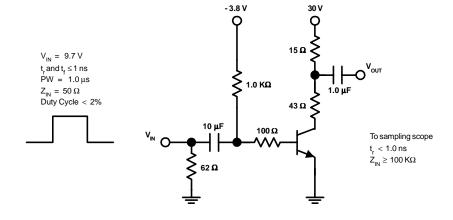


FIGURE 1: Switching Time Test Circuit (I $_{\rm c}$  = 500 mA, I $_{\rm B1}$  = 50 mA, I $_{\rm B2}$  = 50 mA)

#### **TRADEMARKS**

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

 $ACEx^{TM}$ FASTr™ PowerTrench® SyncFET™ Bottomless™ QFET™ TinyLogic™ GlobalOptoisolator™ QSTM UHC™ CoolFET™ GTO™ **VCX**<sup>TM</sup>  $CROSSVOLT^{TM}$ QT Optoelectronics™ HiSeC™

DOME™ ISOPLANAR™ Quiet Series™

#### **DISCLAIMER**

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### PRODUCT STATUS DEFINITIONS

#### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

This datasheet has been download from:

www.datasheetcatalog.com

Datasheets for electronics components.